

WHAT IS CLAIMED IS:

1. A method of testing a memory set having a data write input, a data read output, a plurality of rows and columns, the data write input having a data width larger than the data width of the data read output, the method comprising the steps of:

- 5 selecting one of the plurality of rows;
 writing a unique data pattern to a selected cell within the selected row; and
 reading the unique data pattern from the selected row.

ABSTRACT OF THE DISCLOSURE

- 10 A method and apparatus for accurately testing the addressing of memory devices having memory sets where the data input and data output have width disparities. The method and apparatus ensure that unique patterns are completely written to and read from the memory set in their entirety, thus ensuring that the memory set is valid.
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